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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/770,046	02/02/2004	Parag Parikh	2	9503
7590 07/14/2005			EXAMINER	
Ryan, Mason & Lewis, LLP			KINKEAD, ARNOLD M	
Suite 205				
1300 Post Road Fairfield, CT 06824			ART UNIT	PAPER NUMBER
			2817	,

DATE MAILED: 07/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Comments	10/770,046	PARIKH, PARAG				
Office Action Summary	Examiner	Art Unit				
	Arnold M. Kinkead	2817				
The MAILING DATE of this communi Period for Reply	ication appears on the cover sheet wit	h the correspondence address				
A SHORTENED STATUTORY PERIOD FOTHE MAILING DATE OF THIS COMMUNICATION OF THIS COMMUNICATION OF THIS COMMUNICATION OF THE MAILING DATE OF THIS COMMUNICATION OF THE PROPERTY OF THIS COMMUNICATION OF T	CATION. of 37 CFR 1.136(a). In no event, however, may a reunication. of any any and the statutory minimum of thirty futurory period will apply and will expire SIX (6) MONT will, by statute, cause the application to become ABA	ply be timely filed (30) days will be considered timely. (HS from the mailing date of this communication. NDONED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) file	d on					
2a) This action is FINAL . 2	2b)⊠ This action is non-final.					
3) Since this application is in condition	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practic	ce under <i>Ex parte Quayle</i> , 1935 C.D.	11, 453 O.G. 213.				
Disposition of Claims						
4)⊠ Claim(s) <u>1-31</u> is/are pending in the a	4)⊠ Claim(s) <u>1-31</u> is/are pending in the application.					
4a) Of the above claim(s) is/ar	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.						
	6) Claim(s) 1,2,6,7,23 and 27-29 is/are rejected.					
7) Claim(s) 3,5,8-22,24-26,30 and 31 is						
8) Claim(s) are subject to restric	tion and/or election requirement.					
Application Papers						
9) ☐ The specification is objected to by the	e Examiner.					
10)⊠ The drawing(s) filed on <u>19 April 2004</u>	is/are: a)⊠ accepted or b)□ object	ted to by the Examiner:				
Applicant may not request that any object		` '				
Replacement drawing sheet(s) including						
11)☐ The oath or declaration is objected to	by the Examiner. Note the attached	Office Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) ☐ Acknowledgment is made of a claim f a) ☐ All b) ☐ Some * c) ☐ None of:		119(a)-(d) or (f).				
	documents have been received.					
	documents have been received in Ap					
	of the priority documents have been r	received in this National Stage				
* See the attached detailed Office action	nal Bureau (PCT Rule 17.2(a)).	eceived				
		cocivou.				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Su	immary (PTO-413)				
2) D Notice of Draftsperson's Patent Drawing Review (P	TO-948) Paper No(s)	/Mail Date // /				
 Information Disclosure Statement(s) (PTO-1449 or I Paper No(s)/Mail Date <u>020</u> 	PTO/SB/08) 5) \(\bigcap \text{ Notice of Inf} \) 6) \(\bigcap \text{ Other: } \(\bigcap \)	ormal Patent Application (PTO-/152)				

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DETAILED ACTION

Claim Rejections - 35 USC § 102

 The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1,6,7, and 27 are rejected under 35 U.S.C. 102(b) as being anticipated by Mar (US 6,114,914).

 The ereference by Mar discloses a clock generation circuit that changes(slews) a clock frequency from an intial frequency to a final clock frequency by using a VCO(106) with a plurality of phase outputs(Taps A..N) a modulator(108') coupled to the output of the oscillator(see figure s 4 and 5), whereby an initial clock frequency is used to set a frequency divider value, see col. 4, lines 7-23, where the frequency divider is adjusted to a final frequency divider value as desired using one of the plurality of phase outputs. Please note the feedback clock is "modulated" by way of these steps A-D, as the modulator is updated from the initial period at step A through the last period when the desired output frequency is achieved. The feedback divider (122) and MUX(126) are shown in (108), along with PFD(102), inherent charge pump and loop filter(104). The method steps being inherent.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 2 23,28, and 29 rejected under 35 U.S.C. 103(a) as being unpatentable over Mar(* 914) in view of Mastrocola et al(US 6,356,132).

The ereference by Mar discloses a clock generation circuit that changes(slews) a clock frequency from an initial frequency to a final clock frequency by using a VCO(106) with a plurality of phase outputs(Taps A..N) a modulator(108') coupled to the output of the oscillator(see figure s 4 and 5), whereby an initial clock frequency is used to set a frequency divider value, see col. 4, lines 7-23, where the frequency divider is adjusted to a final frequency divider value as desired using one of the plurality of phase outputs. Please note the feedback clock is "modulated" by way of these steps A-D, as the modulator is updated from the initial period at step A through the last period when the desired output frequency is achieved. The feedback divider (122) and MUX(126) are shown in (108), along with PFD(102), inherent charge pump and loop filter. The method steps being inherent.

The reference does not suggest integration for the clock generator, however, these clock generators are utilized for providing timing in digital circuits and are implemented as the reference by Mastrocola et al serves to highlight this fact, see col. 1, lines 10-28. These circuits are complex and integrating them allows for a more compact clock generation means.

In light of the above it would have been obvious to make the clock circuit of Mar an integrated circuit so as to allow for a more compact and cleaner interface when all the circuit elements are formed on a common substrate. This is noted

in Mastrocola et al.

Allowable Subject Matter

Claims 3,5,8-22,24,25,26, and 30-31 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Arnold M. Kinkead whose telephone number is 571-272-1763. The examiner can normally be reached on Mon-Fri, 8:30 am -5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Arnold M Kinkead

Primary Examiner

Art Unit 2817

Arnold Kinkead July, 08, 2005